

09/817748
Search results

WEST

Freeform Search

Database:

US Patents Full-Text Database
 US Pre-Grant Publication Full-Text Database
 JPO Abstracts Database
 EPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Term:

l1 and coxsackievir\$ near B and immunomodulatory
 and capsid and protease and untranslated

Display:

100

Documents in Display Format:

-

Starting with Number

1

Generate:

☐

Hit List

☒

Hit Count

☐

Side by Side

☐

Image

Search

Clear

Help

Logout

Interrupt

Main Menu

Show S Numbers

Edit S Numbers

Preferences

Cases

Search History

DATE: Saturday, December 13, 2003

[Printable Copy](#)[Create Case](#)Set Name Query
side by sideHit Count Set Name
result set

DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L9</u>	l1 and coxsackievir\$ near B and immunomodulatory and capsid and protease and untranslated	3	<u>L9</u>
<u>L8</u>	l1 and coxsackievir\$ near B and immunomodulatory and capsid and protease untranslated	19295	<u>L8</u>
<u>L7</u>	L2 and ("IL-4" or "IL-10" or cytokine\$)	4	<u>L7</u>
<u>L6</u>	L5 and (Th1 or Th2)	1	<u>L6</u>
<u>L5</u>	20030190329	1	<u>L5</u>
<u>L4</u>	L2 and (Th1 or Th2) and Th1	3	<u>L4</u>
<u>L3</u>	L2 and (Th1 or Th2)	3	<u>L3</u>
<u>L2</u>	attenuat\$ near10 coxsackievir\$ or CBV near10 vector\$	36	<u>L2</u>
<u>L1</u>	coxsackievir\$ or CBV near10 vector\$	946	<u>L1</u>

END OF SEARCH HISTORY

[Generate Collection](#)[Print](#)**Search Results - Record(s) 1 through 36 of 36 returned.**

-
- ☐ 1. 20030190329 . 26 Mar 01. 09 Oct 03. Cocksackievirus vectors and their use in prevention and treatment of disease. Tracy, Steven M., et al. 424/208.1; 424/216.1 A61K039/21 A61K039/125 C12N015/867.
-
- ☐ 2. 20030135495 . 21 Jun 02. 17 Jul 03. Database indexing method and apparatus. Vagnozzi, Paul P.. 707/3; G06F007/00.
-
- ☐ 3. 20030065871 . 28 Sep 01. 03 Apr 03. Multiple I/O path selection among disparate channel paths. Casper, Daniel F., et al. 710/316; G06F013/00.
-
- ☐ 4. 20020197711 . 12 Jun 01. 26 Dec 02. Cocksackievirus B4 expression vectors and uses thereof. Ramsingh, Arlene I., et al. 435/320.1; 424/148.1 424/199.1 424/207.1 435/5 435/69.7 435/91.1 435/91.33 C12Q001/70 C12P021/04 C12P019/34 A61K039/42 A61K039/12 A61K039/21 C12N015/00 C12N015/09 C12N015/63 C12N015/70 C12N015/74.
-
- ☐ 5. 20020164598 . 03 May 01. 07 Nov 02. Method for evaluating and applying an individual's genetic characteristics to determine response to cardiovascular medication therapy. Muhlestein, Joseph B., et al. 435/6; 702/20 705/3 C12Q001/68 G06F017/60 G06F019/00 G01N033/48 G01N033/50.
-
- ☐ 6. 6594733 . 27 Sep 00; 15 Jul 03. Cache based vector coherency methods and mechanisms for tracking and managing data use in a multiprocessor system. Cardente; John T.. 711/145; 711/141 711/144. G06F012/08.
-
- ☐ 7. 6351804 . 10 Oct 00; 26 Feb 02. Control bit vector storage for a microprocessor. Pflum; Marty L.. 712/217; 712/213 712/228 712/23 712/245. G06F009/30 G06F009/305 G06F009/22.
-
- ☐ 8. 6323024 . 27 Mar 00; 27 Nov 01. Cocksackie virus vectors for delivery of nucleic acids encoding antigenic or therapeutic products. Tracy; Steven M., et al. 435/320.1; 424/93.1 424/93.2 424/93.6 435/235.1 435/366 435/455 435/456 435/69.1 536/23.5 536/23.51 536/23.52 536/24.1. C12N015/86 C12N015/63 C12N015/19 C07H021/04.
-
- ☐ 9. 6269436 . 08 Sep 99; 31 Jul 01. Superscalar microprocessor configured to predict return addresses from a return stack storage. Tran; Thang M., et al. 712/23; 712/228 712/229 712/243. G06F015/00.
-
- ☐ 10. 6189068 . 28 Jun 99; 13 Feb 01. Superscalar microprocessor employing a data cache capable of performing store accesses in a single clock cycle. Witt; David B., et al. 711/3; 711/118 711/204 712/218 712/23. G06F015/76.
-
- ☐ 11. 6157994 . 08 Jul 98; 05 Dec 00. Microprocessor employing and method of using a control bit vector storage for instruction execution. Pflum; Marty L.. 712/23; 712/202 712/217 712/218 712/9. G06F009/30 G06F009/312 G06F009/38.
-
- ☐ 12. 6079006 . 31 Mar 98; 20 Jun 00. Stride-based data address prediction structure. Pickett; James K.. 711/213; 711/221 712/237 712/239. G06F009/38 G06F009/32 G06F009/355.
-
- ☐ 13. 6071742 . 05 Mar 97; 06 Jun 00. Cocksackie virus as a vector for delivery of anti-inflammatory cytokines. Tracy; Steven M., et al. 435/320.1; 536/23.5 536/23.51 536/23.52.

C12N015/86.

-
- ☐ 14. 6014734 . 15 Sep 98; 11 Jan 00. Superscalar microprocessor configured to predict return addresses from a return stack storage. Tran; Thang M., et al. 712/23; 712/228 712/229 712/243. G06F009/38.
-
- ☐ 15. 5987561 . 03 Jun 97; 16 Nov 99. Superscalar microprocessor employing a data cache capable of performing store accesses in a single clock cycle. Witt; David B., et al. 711/3; 711/128 711/204 712/218 712/23. G06F015/76.
-
- ☐ 16. 5978907 . 06 Oct 98; 02 Nov 99. Delayed update register for an array. Tran; Thang M., et al. 712/239;. G06F009/42.
-
- ☐ 17. 5935239 . 17 Jul 98; 10 Aug 99. Parallel mask decoder and method for generating said mask. Narayan; Rammohan. 712/224; 712/213 712/23 712/233. G06F009/30.
-
- ☐ 18. 5933618 . 30 Oct 95; 03 Aug 99. Speculative register storage for storing speculative results corresponding to register updated by a plurality of concurrently recorded instruction. Tran; Thang M., et al. 712/217; 712/212 712/213 712/218. G06F009/38.
-
- ☐ 19. 5892936 . 20 Jun 97; 06 Apr 99. Speculative register file for storing speculative register states and removing dependencies between instructions utilizing the register. Tran; Thang M., et al. 712/216; 712/212 712/213 712/23. G06F009/38.
-
- ☐ 20. 5881278 . 30 Oct 95; 09 Mar 99. Return address prediction system which adjusts the contents of return stack storage to enable continued prediction after a mispredicted branch. Tran; Thang M., et al. 712/242; 712/238. G06F009/32.
-
- ☐ 21. 5878255 . 12 Nov 97; 02 Mar 99. Update unit for providing a delayed update to a branch prediction array. Tran; Thang M., et al. 712/240;. G06F009/40.
-
- ☐ 22. 5875324 . 08 Oct 97; 23 Feb 99. Superscalar microprocessor which delays update of branch prediction information in response to branch misprediction until a subsequent idle clock. Tran; Thang M., et al. 712/238; 712/240. G06F009/38.
-
- ☐ 23. 5875315 . 20 Aug 97; 23 Feb 99. Parallel and scalable instruction scanning unit. Narayan; Rammohan. 712/204;. G06F009/30.
-
- ☐ 24. 5864707 . 11 Dec 95; 26 Jan 99. Superscalar microprocessor configured to predict return addresses from a return stack storage. Tran; Thang M., et al. 712/23; 712/228 712/243. G06F009/38.
-
- ☐ 25. 5860104 . 31 Aug 95; 12 Jan 99. Data cache which speculatively updates a predicted data cache storage location with store data and subsequently corrects mispredicted updates. Witt; David B., et al. 711/137; 711/126 711/204 711/213. G06F009/38.
-
- ☐ 26. 5859991 . 08 Jan 97; 12 Jan 99. Parallel and scalable method for identifying valid instructions and a superscalar microprocessor including an instruction scanning unit employing the method. Narayan; Rammohan. 712/204;. G06F015/00.
-
- ☐ 27. 5854921 . 31 Aug 95; 29 Dec 98. Stride-based data address prediction structure. Pickett; James K.. 712/239; 712/1 712/23. G06F009/37.
-
- ☐ 28. 5826071 . 31 Aug 95; 20 Oct 98. Parallel mask decoder and method for generating said mask.

Narayan; Rammohan. 712/224; 712/23. G06F013/00.

☐ 29. 5822560 . 23 May 96; 13 Oct 98. Apparatus for efficient instruction execution via variable issue and variable control vectors per issue. Pflum; Marty L.. 712/214; 712/208. G06F009/38.

☐ 30. 5822559 . 02 Jan 96; 13 Oct 98. Apparatus and method for aligning variable byte-length instructions to a plurality of issue positions. Narayan; Rammohan, et al. 712/214; 712/213. G06F009/30.

☐ 31. 5802582 . 10 Sep 96; 01 Sep 98. Explicit coherence using split-phase controls. Ekanadham; Kattamuri, et al. 711/152; 710/200 711/141 711/169. G06F012/14.

☐ 32. 5790821 . 08 Mar 96; 04 Aug 98. Control bit vector storage for storing control vectors corresponding to instruction operations in a microprocessor. Pflum; Marty L.. 712/200; 712/202 712/204 712/217 712/218 712/220 712/23 712/4 712/7. G06F009/30 G06F009/38.

☐ 33. 5781789 . 31 Aug 95; 14 Jul 98. Superscaler microprocessor employing a parallel mask decoder. Narayan; Rammohan. 712/23; 712/206 712/210 712/215 712/233. G06F009/30.

☐ 34. 5752069 . 31 Aug 95; 12 May 98. Superscalar microprocessor employing away prediction structure. Roberts; James S., et al. 712/23; 711/128 711/204 711/213 712/207 712/219. G06F012/12 G06F012/10.

☐ 35. JP 05161020 A . 05 Dec 91. 25 Jun 93. VECTOR QUANTIZATION METHOD AND DEVICE THEREFOR. NAKANO, KEIICHI. H04N001/415; G06F015/66 H04N007/13.

☐ 36. US 20020197711 A1 . New recombinant attenuated coxsackievirus B4 virion, useful for inducing an immune response, particularly for treating or preventing viral infections, or for protecting HIV-infected individuals from progression to AIDS. HALIM, S S, et al. A61K039/12 A61K039/21 A61K039/42 C12N015/00 C12N015/09 C12N015/63 C12N015/70 C12N015/74 C12P019/34 C12P021/04 C12Q001/70.

Generate Collection

Print

Terms	Documents
attenuat\$ near10 coxsackievir\$ or CBV near10 vector\$	36

[Previous Page](#)

[Next Page](#)



Day : Saturday
Date: 12/13/2003

Time: 17:10:34

Inventor Name Search

Enter the **first few letters** of the Inventor's Last Name.
Additionally, enter the **first few letters** of the Inventor's First name.

Last Name

First Name

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)



Day : Saturday
Date: 12/13/2003

Time: 17:10:34

Inventor Name Search

Enter the **first few letters** of the Inventor's Last Name.
Additionally, enter the **first few letters** of the Inventor's First name.

Last Name

First Name

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)